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Patent

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Qing Ma et al.

Serial No.: Not yet assigned

Filed: Herewith

For: METHOD FOR MAKING A
SEMICONDUCTOR DEVICE HAVING A
LOW-K DIELECTRIC LAYER

Assistant Commissioner for Patents
and Trademarks
Washington, D.C. 20231

)
) Examiner: Not yet assigned
)
) Art Unit: Not yet assigned
)
)

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FORMAL DRAWINGS SUBMISSION

Enclosed herewith for submission in the United States Patent and Trademark Office are two (2) sheets of formal drawings for the patent application referenced above.

If there are any further charges please charge them to Deposit Account No. 02-2666. A duplicate of this sheet is enclosed for that purpose.

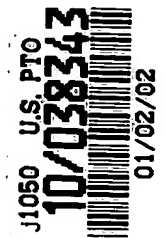
Respectfully submitted,

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Dated: 1/2/02 ^{maB} 200T

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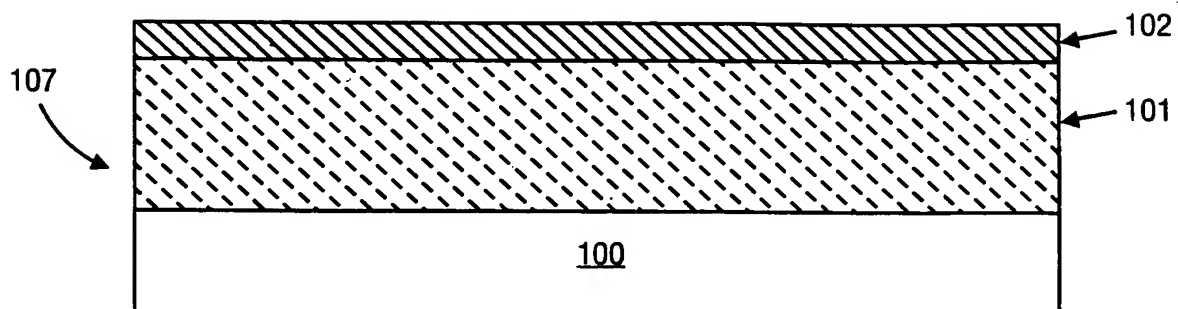


FIG. 1a

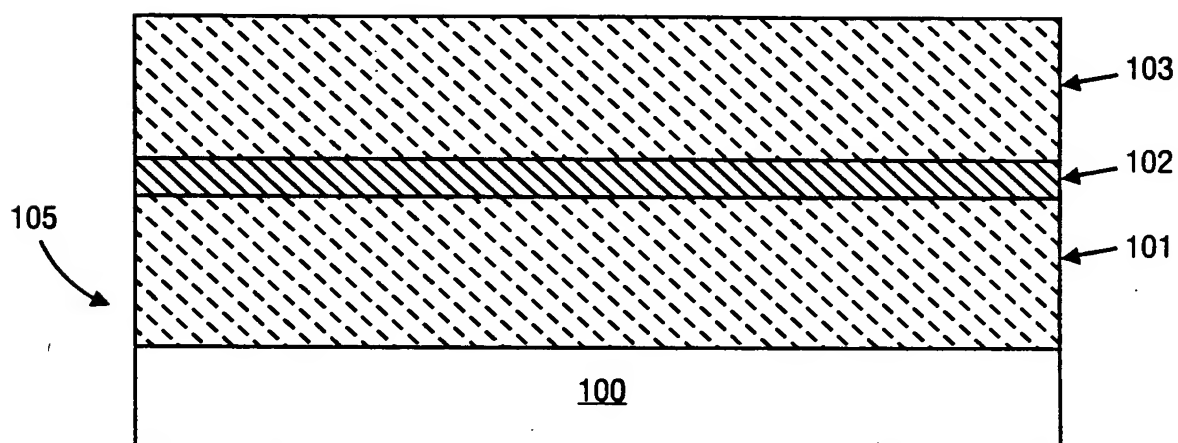


FIG. 1b

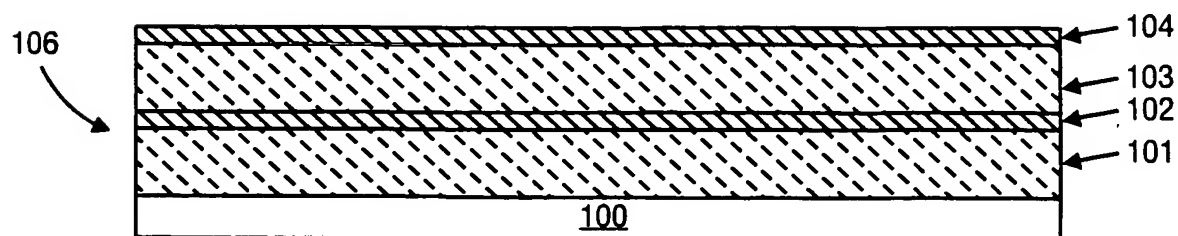


FIG. 1c

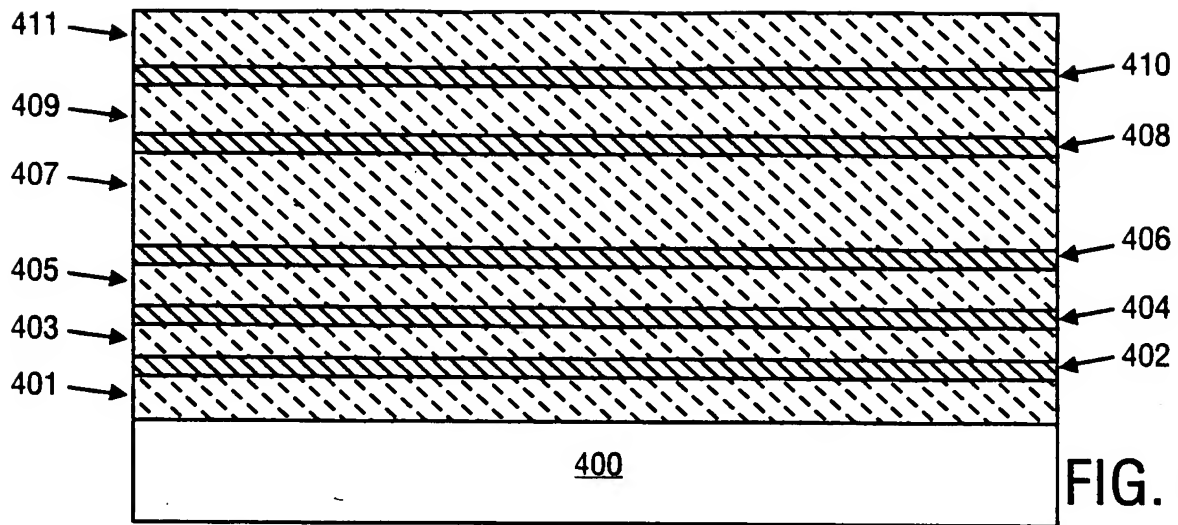


FIG. 2a

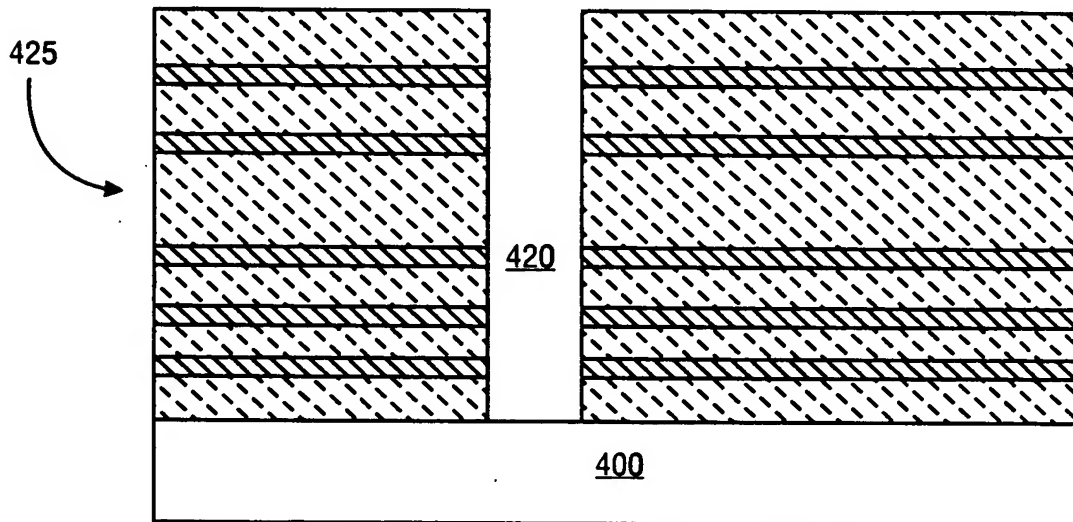


FIG. 2b

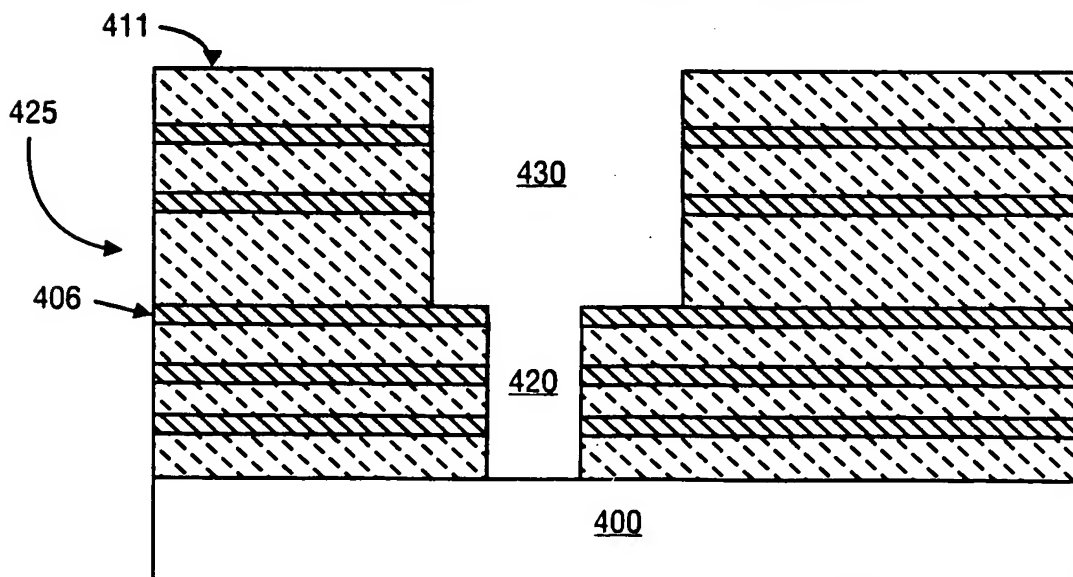


FIG. 2c

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